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				Filing Date	July 12, 2005
				First Named Inventor	Robert DWILINSKI
				Art Unit	2881
				Examiner Name	M. J. Logie
Sheet	1	of	1	Attorney Docket Number	204552033800

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
1.	US-6,031,858	02-29-2000	Hatakoshi et al.		
2.	US-2001/0030328-A1	10-18-2001	Ishida		
3.	US-7,057,211	06-06-2006	Dwilinski et al.		

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
1.	Sze, S.M. (1998). "Appendix G: Properties of Important Element and Binary Semiconductors," in <i>Modern Semiconductor Device Physics</i> . John Wiley & Sons, Inc. New York. Esp. pp. 539-540				
2.	M. Fukuda. (1999) <i>Optical Semiconductor Devices</i> : Wiley Series in Microwave and Optical Engineering. Chang, K., John Wiley & Sons, Inc. New York. Esp. pp. 7				
3.	U.S. Office Action, mailed April 15, 2008, directed to related U.S. Patent Application No. 10/493,594. 27 pages				

Examiner Signature	Date Considered
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